Scache SBE, failing bit SRAM.

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Symptom(s):

ESP Class Description: RAS
Scache SBE, failing bit SRAM.
ESP Event Type Id: 359
Example symptom - Syslog Message: WARNING: /hw/module/003c16/node/cpubus/0/b: INTR: Scache SBE, failing bit 88 CPU C0C3.AL3 SRAM D5C7.D6
ESP Type Description: secondary Cache SBE

Fact(s):

SGI Origin 3000
IRIX 6.5.X
Scache SBE
Secondary cache

SGI Solution:

Replace reporting PIMM following guide lines in TIB 200141, see below.

TIB 200141: Global Product Support (GPS) has established a maximum allowable failure rate for the field that should occur before a node board is replaced. Currently, GPS suggests a maximum of 3 SBEs per CPU within 24 hours at the same SRAM location.

Additional Details

Creation Date	07/13/2001 02:18 AM
Last modification	09/19/2015 07:20 PM
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